REMARKS

Applicant respectfully requests the Examiner's reconsideration of the present application.

Applicant wishes to thank Examiner Ahn for discussing the issues presented in the Office Action mailed 6/16/2009 with Applicant's representative, Lawrence Cho, over the phone on October 6, 2009. The Office's objection to the drawings and specification were discussed. Applicant's representative agreed to address these objections in a response.

Claims 1-9, 11-15, 17-23, 25-27, 29-35, 37-50 are pending in the present application.

Claims 1-9, 11-15, 17-23, 25-27, 29-35, 37-50 are allowed.

The drawings are objected to under 37 CFR §1.83(a).

The specification is objected to due to informalities.

The drawings are objected to under 37 CFR §1.83(a). Specifically, the Office Action mailed 6/16/2009 states in part the following.

> The drawings must show every feature of the invention specified in the claims. Therefore, the "synchronization point that identifies an amount of delay incurred from transmission of the sample sequences from the correlation outputs" must be shown or the feature(s) canceled from the claim(s)."

(6/16/2009 Office Action, p. 2).

Applicant respectfully submits that the drawings do show every feature of the invention specified in the claims. For example, Applicant refers the Office to the correlation output processor 170 in the parallel samples, parallel coefficients, time division multiplexer (PPT) correlator 100 illustrated in Figure 1. Pages 11 and 12 of the specification describe the function of the correlation output processor 170 as follows.

> The correlation output processor 170 <u>determines a synchronization point</u> in a sample from the correlation outputs. According to an embodiment of the PPT correlator 100, the <u>correlation output processor 170 determines that the first value of a sample sequence having a correlation output with the highest numerical value is the synchronization point</u>.

Serial No. 09/891,710   11   ALT.P001 (A00651)

(Specification, pp. 11-12) (Emphasis Added).

Applicant further refers the Office to Figure 4 which is a flow chart illustrating a method
for managing a code sequence according to an embodiment of the present invention. Block 408
in Figure 4 is labeled "DETERMINE A SYNCHRONIZATION POINT FOR THE CODE
SEQUENCE". Page 17 of the specification describes the procedure of block 408 as follows.

> At step 408, a synchronization point for the code sequence is
> determined from the correlation outputs. According to an
> embodiment of the present invention, determining a
> synchronization point comprises determining a correlation
> output having a highest numerical value:

(Specification, p. 17) (Emphasis Added).

Applicant further refers the Office to Figure 10 which illustrates a table of an exemplary
code sequence, received sample, and correlation outputs calculated for sample sequence groups
from the received sample. Page 28 of the specification states the following.

> The correlation output processor 170 would determine that the
> first sample value in the second sample sequence, 2, is the
> synchronization point for the code sequence.

(Specification, p. 28) (Emphasis Added).

Applicant respectfully submits that the examples provided from the drawings illustrate 1)
an apparatus that determines a synchronization point, 2) a method for determining a
synchronization point, and 3) an example of a synchronization point, and therefore the drawings
of the present application comply with 37 CFR §1.83(a).

The specification is objected to due to informalities. Specifically, the Office Action
mailed 6/16/2009 states in part the following.

> The disclosure is objected to because of the following
> informalities: the specification on page 9, lines 13-16 appear to
> repeat the claimed limitation of "synchronization point that
> identifies an amount of delay incurred from transmission of the
> sample sequences from the correlation outputs". In several parts
> of the specification describes wherein the synchronization point
> is determined based on the highest correlation values, for
> example description related to Figure 10. However, the
> specification does not further describe the relationship between

Serial No. 09/891,710   12   ALT.P001 (A00651)

> the highest correlation values and the "amount of delay incurred
> from transmission" in order to arrive at the limitation of
> "synchronization point that identifies an amount of delay
> incurred from transmission of the sample sequence from the
> correlation outputs".

(6/16/2009 Office Action).

Applicant respectfully refers the Office to Figure 10 which illustrates a table of an

exemplary code sequence, received sample, and correlation outputs calculated for sample

sequence groups from the received sample. Pages 27 and 28 of the specification state the

following.

> In this example, the correlation output processor 170 (shown in
> Figure 1), would determine that the correlation output
> corresponding to the second sample sequence has the highest
> numerical value. The correlation output processor 170 would
> determine that the first sample value in the second sample
> sequence, 2, is the synchronization point for the code sequence.

(Specification, p. 28).

Referring to Figure 10, since the second sample sequence, 2, is the "synchronization

point", it is the starting point for which received samples are processed. The amount of delay

incurred from the transmission of sample sequences would therefore be the delay associated for

transmitting the first sample value 5.

Applicant respectfully submits that the example in Figure 10 shows the relationship

between the synchronization point and the amount of delay incurred from transmission, and

therefore the specification of the present patent application complies with all applicable patent
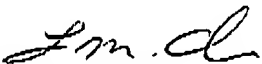
statutes.

In view of the points of clarification provided herein, it is respectfully submitted that the

applicable objections have been overcome.

The Examiner is invited to telephone Applicant's attorney (217-377-2500) to facilitate

prosecution of this application.

Serial No. 09/891,710   13   ALT.P001 (A00651)

If any additional fee is required, please charge Deposit Account No. 50-1624.

Respectfully submitted,

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Serial No. 09/891,710   14   ALT.P001 (A00651)